

FIGURE 1

DRAWING FOR PATENT APPLICATION  
DARBY & DARBY P.C.  
USPTO CUSTOMER NO. 38845  
ATTORNEY DOCKET NO: 08211/0200375-USO  
INVENTOR: Ha Chu Vu  
TITLE: GENERATING ADJUSTABLE-DELAY CLOCK  
SIGNAL FOR PROCESSING COLOR SIGNALS  
2/14

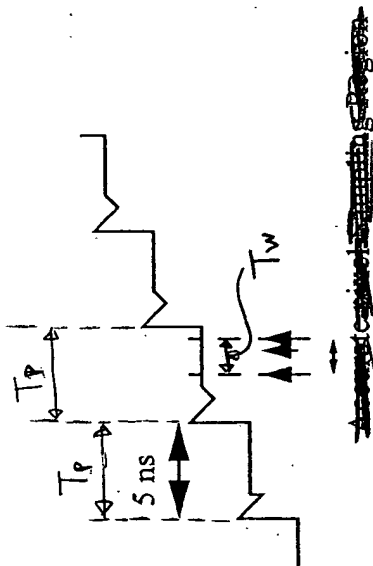


FIGURE 2

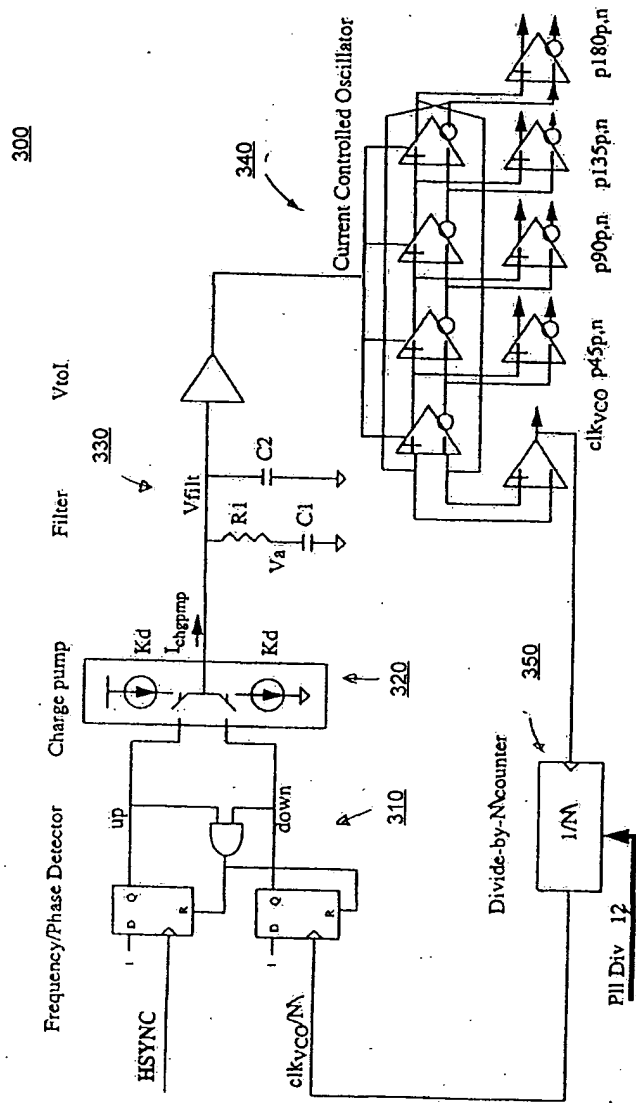


FIGURE 3

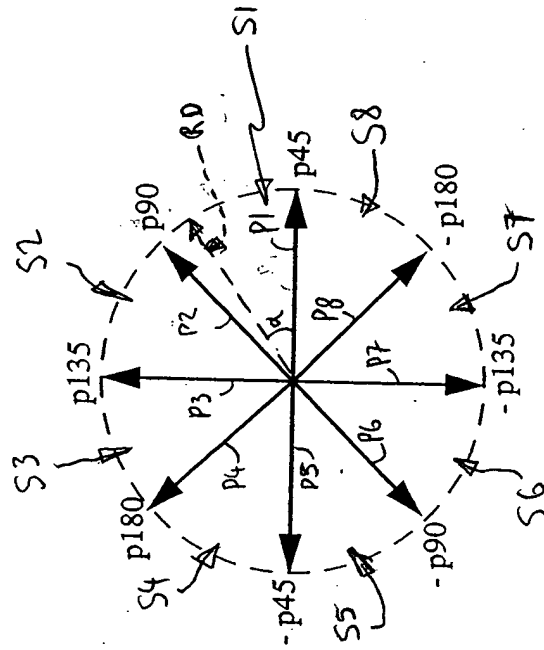


FIGURE 4

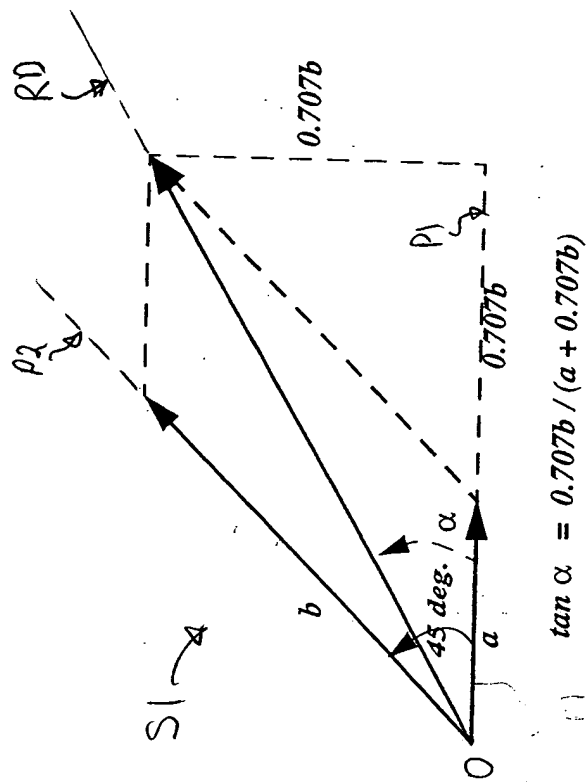
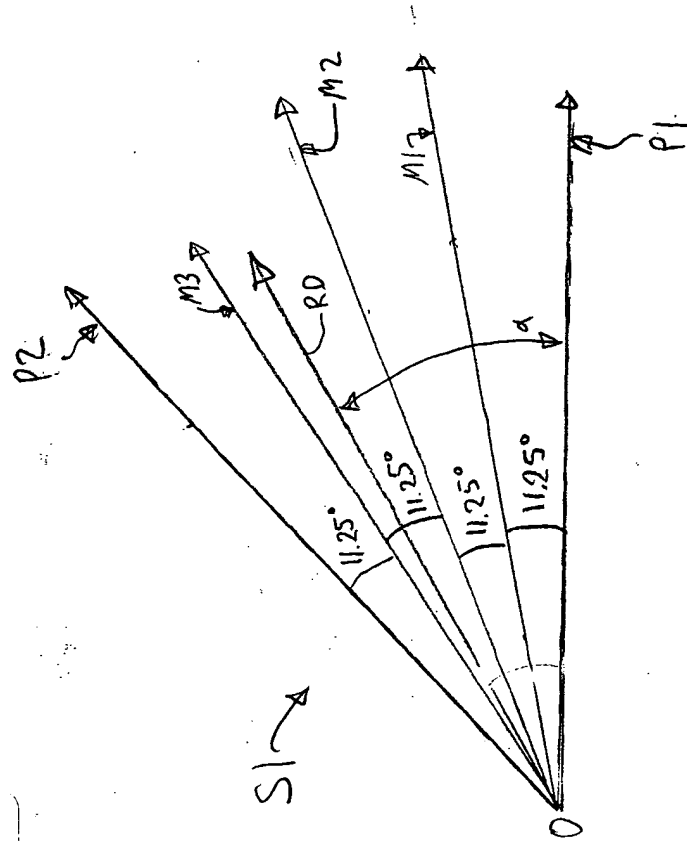


FIGURE 5



700

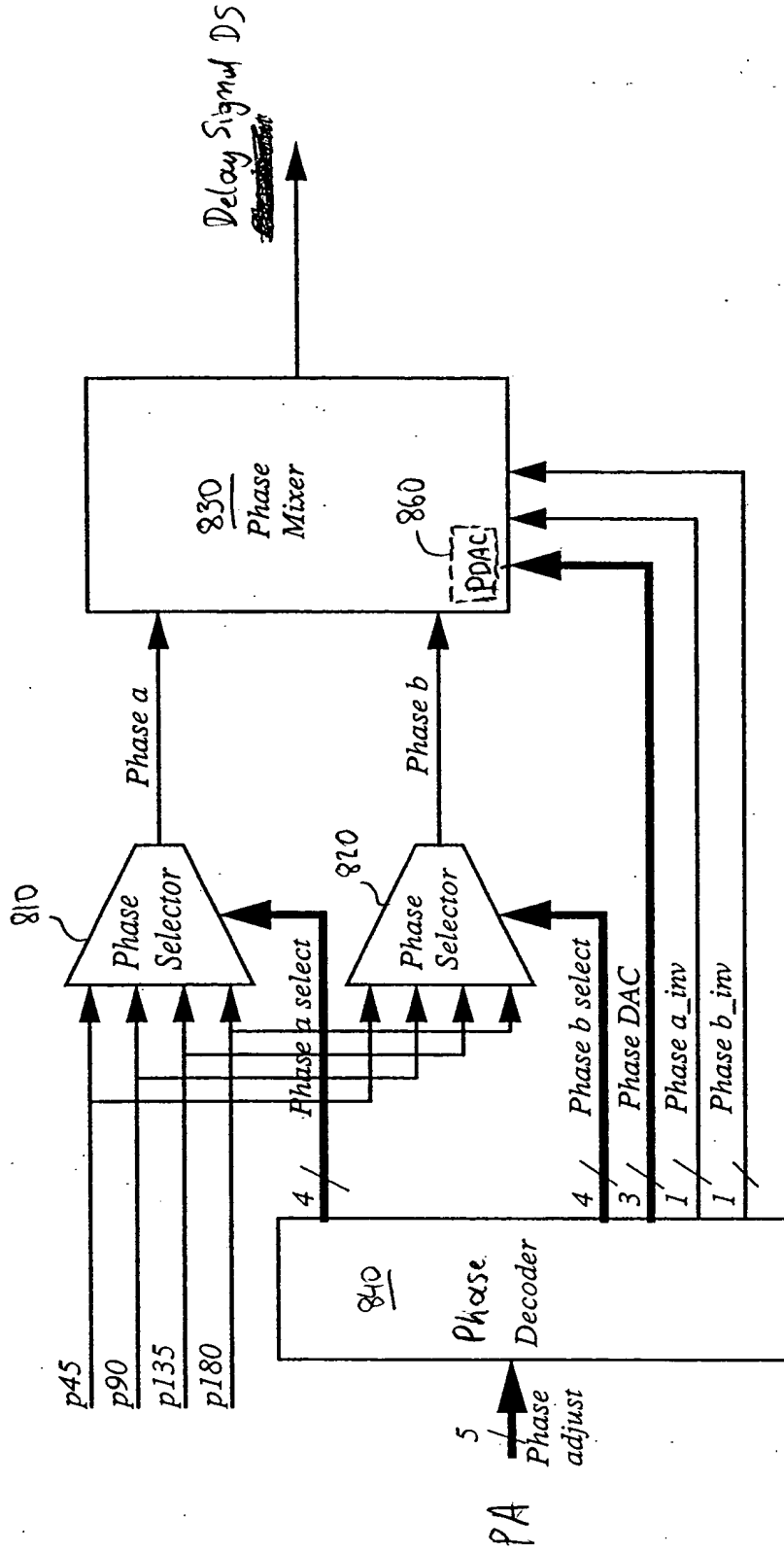
PHASE	$\alpha$ deg.	SET 1		SET 2	
		a	b	a	b
P1	0	16	0	4	0
M1	11.25	12	4	3	1
M2	22.5	8	8	2	2
M3	33.75	4	12	1	3
P2	45	0	16	0	4
		TOTAL = 16		TOTAL = 4	

Phase Step =  $360 \text{ deg} / 32 = 11.25 \text{ deg}$   
 $45 \text{ deg} / 4 = 11.25 \text{ deg}$

WEIGHT VALUES FOR  
SIMULATED PHASES

FIGURE 7

800



PHASE ADJUSTER

FIGURE 8





840

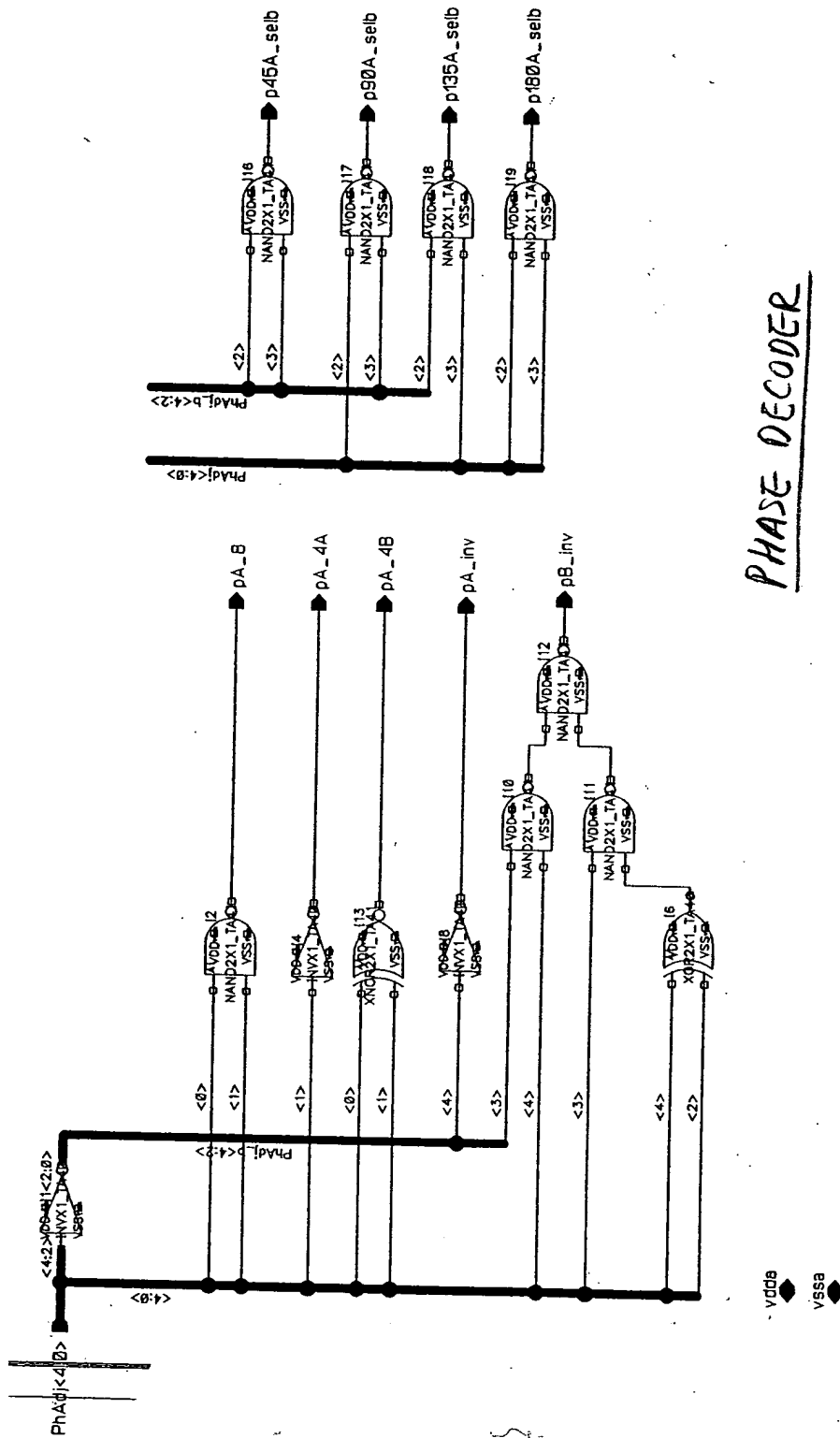


FIGURE 10

PHASE DECODER



# PHASE SELECTOR CHIP

1200

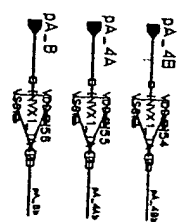
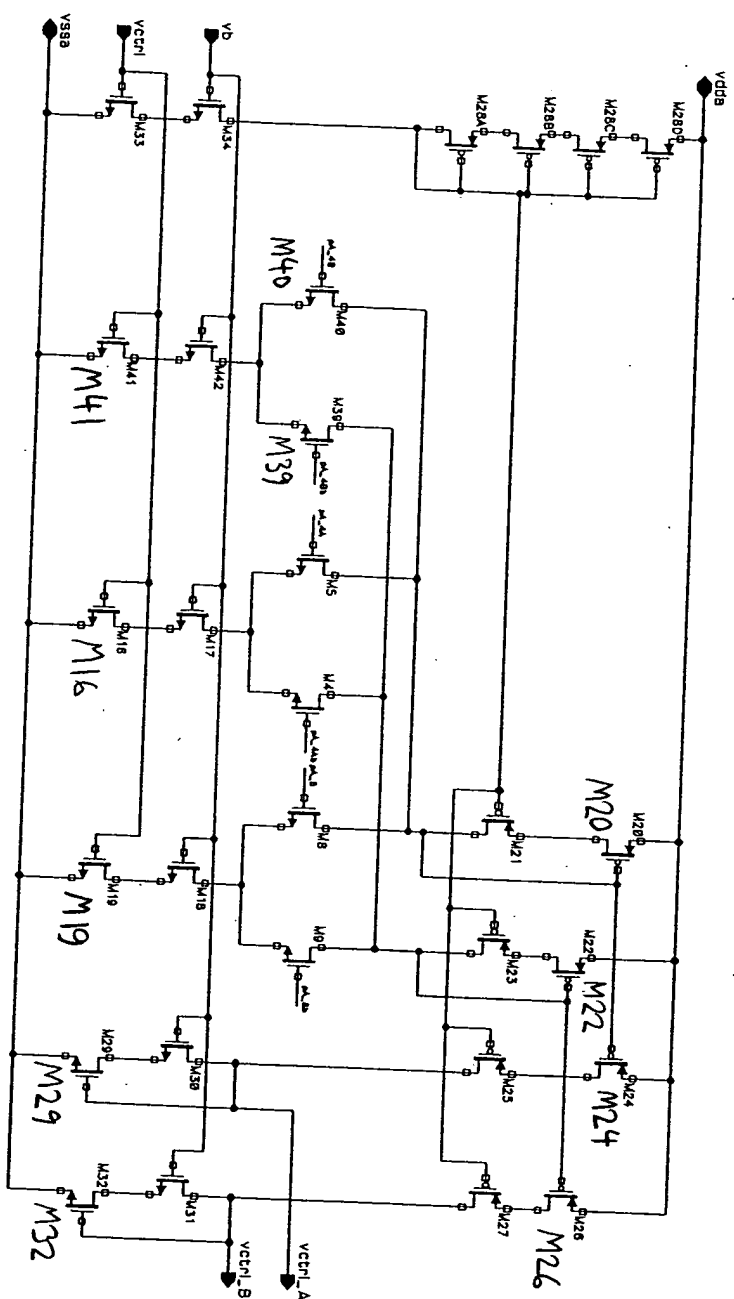


FIGURE 12

PDAC

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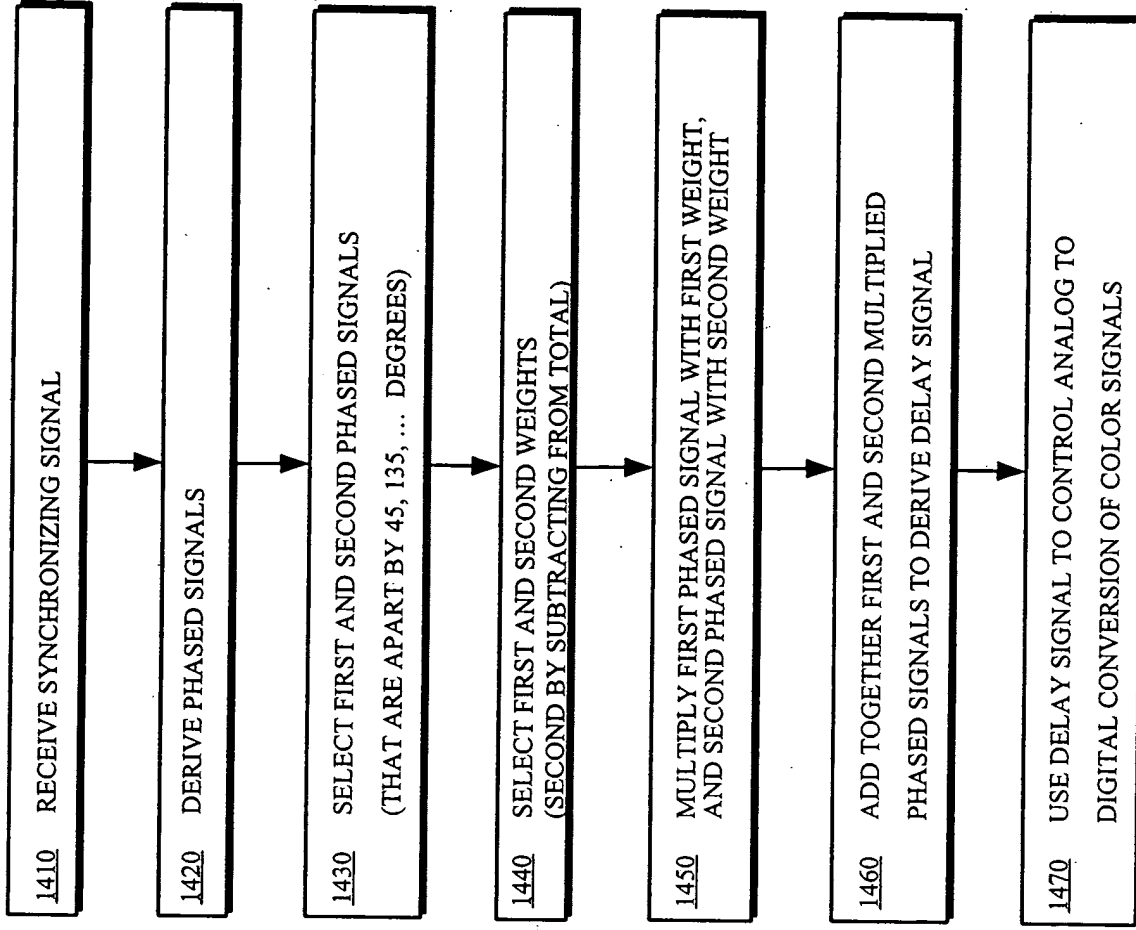


FIGURE 14